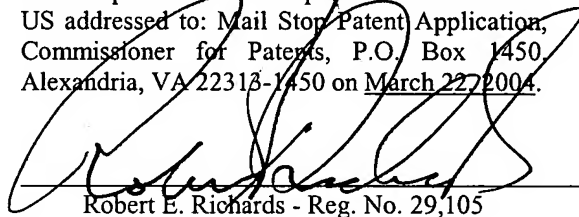


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Makoto Yamamoto et al.)
) Art Unit: **2814**
 Serial No. **To Be Assigned**)
) Examiner: **Shrinivas H. Rao**
 Filed: **Concurrently Herewith**)
 (Divisional Application of 10/014,949 filed October 26, 2001))
)
 For: **Method of Fabricating Semiconductor Integrated**)
Circuit)

I hereby certify that this correspondence is being deposited with the United States Postal Service in an Express Mail envelope No. EL 999329032 US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 22, 2004.



Robert E. Richards - Reg. No. 29,105

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
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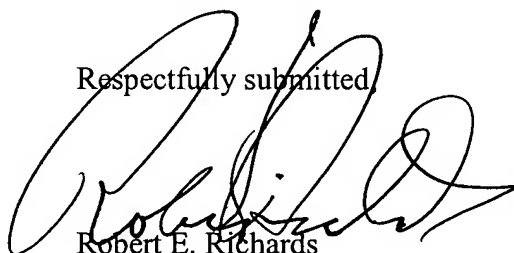
Sir:

The citation of information on the attached Form PTO/SB/08A, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each non-U.S. patent document listed is enclosed.

Information Disclosure Statement
Divisional Application of Application No. 10/014,949
Page 2 of 2

The citation of this information does not constitute an admission of priority or that any cited item is available as a reference, or a waiver of any right the applicant may have under applicable statutes, Rules of Practice in patent cases, or otherwise.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Robert E. Richards", is written over the typed name.

Robert E. Richards
Reg. No. 29,105

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Atlanta, Georgia 30309-4530
Our Docket: 44471-298721

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	Not Yet Assigned
Filing Date	March 22, 2004
First Named Inventor	Makoto Yamamoto
Art Unit	2814
Examiner Name	Rao, Shrinivas H.
Attorney Docket Number	44471/298721

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US- 4,196,440	4-1-1980	Anantha et al.	
	2	US- 4,326,212	4-20-1982	Bergeron et al.	
	3	US- 5,665,994	9-9-1997	Palara	
	4	US- 5,708,290	1-13-1998	Cacciola et al.	
	5	US- 6,242,793	6-5-2001	Colombo et al.	
	6	US- 6,033,947	3-7-2000	Cacciola et al.	
	7	US- 6,225,181	5-1-2001	Gregory	
	8	US- 4,149,906	4-17-1979	De La Moneda	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	9	EP 0 545 488 A1	6-9-93	SGS-Thomson Microelectronics s.r.l.		

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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